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(54) CHANNEL ADAPTIVE EQUALIZATION PRECODING SYSTEM AND METHOD

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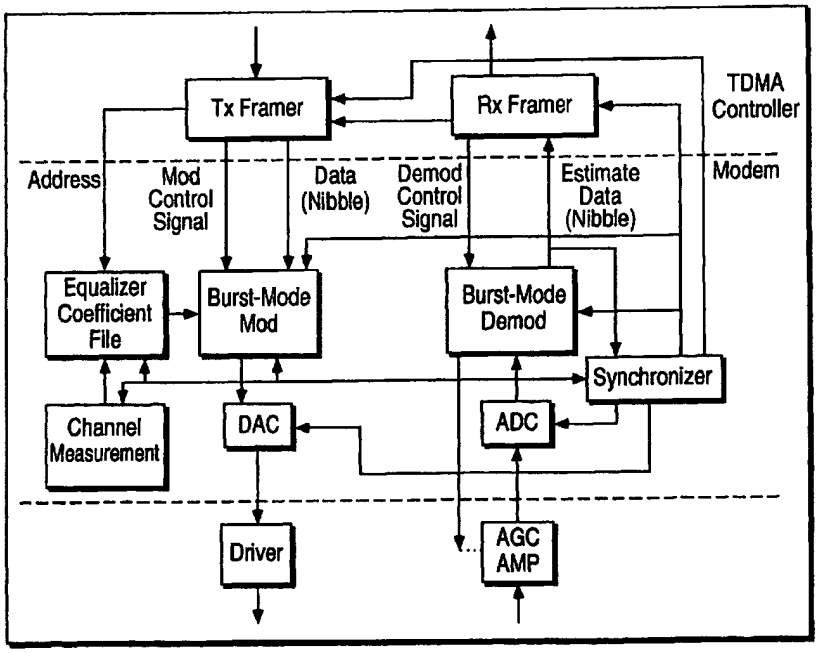
(57) ABSTRACT

A system and method for delivering increased speed, security, and intelligence to wireline and wireless systems. The present invention includes a new generation Fast Circuit Switch (packet/circuit) Communication processors and platform which enables a new Internet Exchange Networking Processor Architecture at the edge and core of every communication system, for next generation Web Operating System or Environment (WOE) to operate on with emphasis of a non-local processor or networking processor with remote web computing capabilities.



Burst-Mode Modem Block Diagram

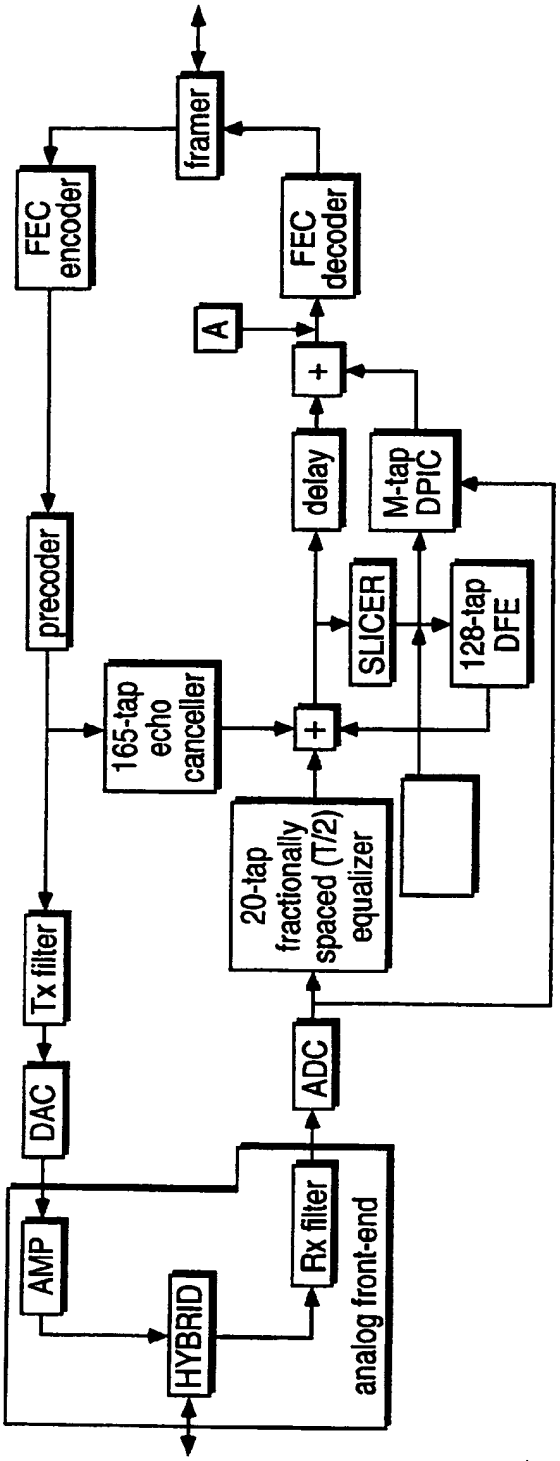


FIG. 31

SNR Measurement Points (A,B) (Proposed Transceiver Structure using DPIC)

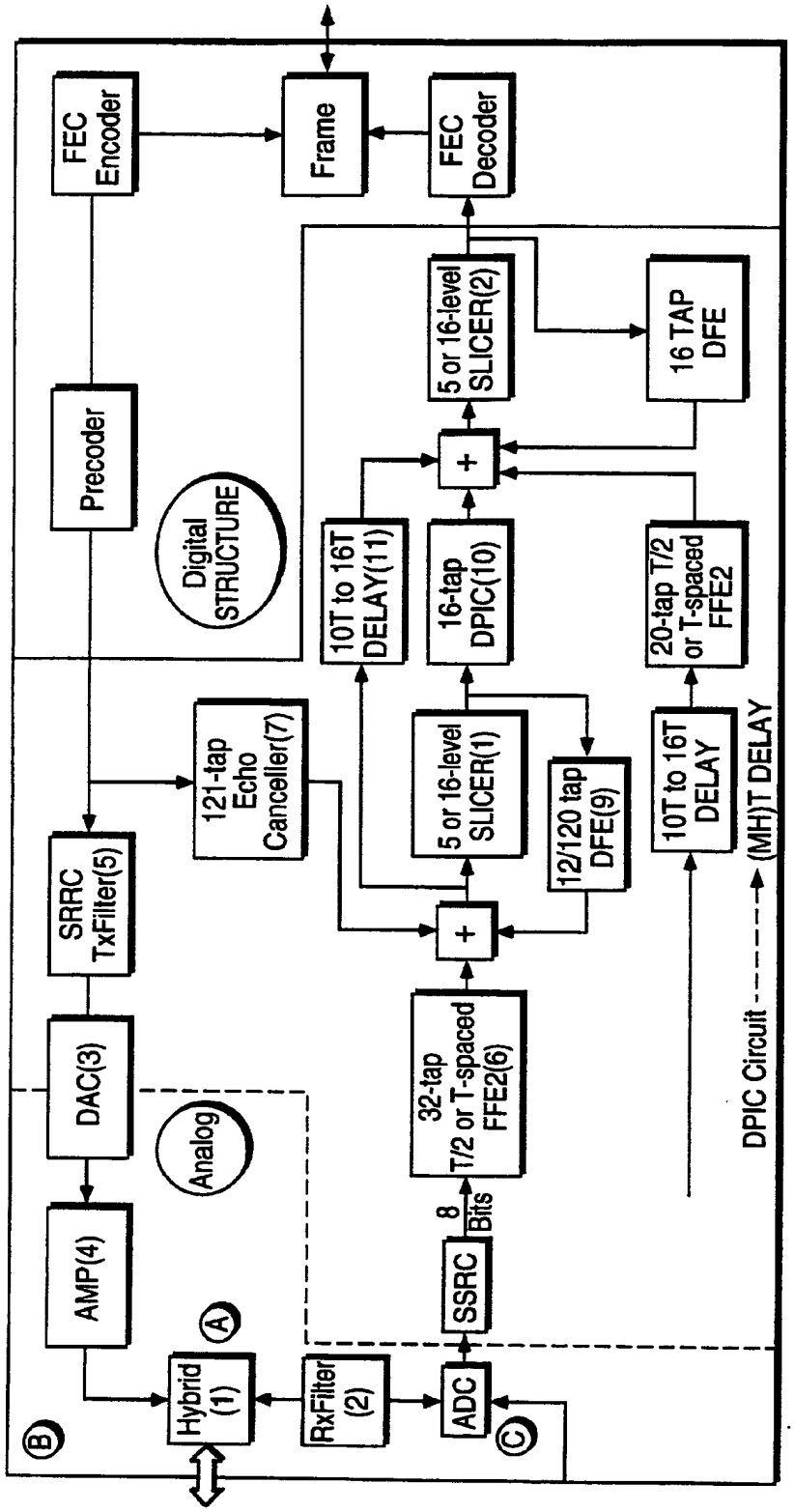


FIG. 32

HDSL2 Front-End (Converter & Sampler & Equalizers)